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#### PATENT APPLICATION

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Application of:

Examiner: K. Coulter

U.S. Patent No. 5,809,306

Group Art Unit: 2784

Serial No.: 09/662,484

August 29, 2001

Reissue Filed: September 14, 2000

For: VARIABLE ADDRESS LENGTH COMPILER AND

PROCESSOR IMPROVED IN ADDRESS

**MANAGEMENT** 

Irvine, California 92614

### INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure as set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97(c) - 1.98, applicant wishes to bring to the attention of the United States Patent Examiner the following references:

# **U.S. PATENTS**

4,314,332	Shiraogawa et al.
4,361,868	Kaplinsky
4,447,879	Fong
4,453,212	Gaither et al.
4,602,330	Ikeya
4,679,140	Gotou et al.
4,739,471	Baum et al.
4,763,255	Hopkins et al.
5,307,492	Benson
5,420,992	Killian et al.
5,440,701	Matsuzaki et al.*

# **FOREIGN REFERENCES**

 0180077
 Europe

 0503514
 Europe

 62-259140
 Japan

 3248240
 Japan

 WO9215943
 PCT

### **OTHER REFERENCES**

Hennessy et al., Computer Architecture . . ., 1990 pp. 90-113 and 139-161.

Sebesta, Concepts of Programming Languages, 1989 pp. 16-23, 72-79 and 106-115, 121-122.

Harman et al., The Motorola MC.68000 . . ., 1985 pp. 30-31, 52-53, 84-89, 134-137, 142-159, 174-177 & 200-203

"Address Size Independence in a 16-Bit Minicomputer", by Philip E. Stanley, the 5th Annual Symposium on Computer Architecture, Apr. 1978.

"High-Performance Simgle-Chip Microncontroller H8/300 Series, by Nobuo Shibasaki et al., Hitachi Review, vol. 40 (1991), No. 1.

The references listed on the attached PTO Form 1449 were cited during the prosecution of the parent case and only a copy of Matsuzaki et al., U.S. Patent No. 5,440,701 is enclosed herewith as it is a new citation.

If the Examiner believes that a telephone interview would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number. In the event an Office Action is mailed before the IDS is received by the Patent Office, please charge the fee of \$180.00 to deposit Account No. 16-2462.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on August 29, 2001.

By: Melissa Sanchez

elissa

Signature

Date: August 29, 2001

Respectfully submitted,

PRICE AND GESS

Bradley D. Blanche Registration No. 38,387

2100 S.E. Main St., Suite 250

Irvine, California 92614

Telephone: (949) 261-8433